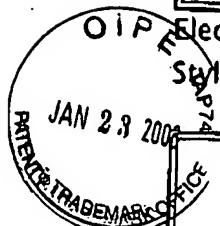


## ELECTRONIC INFORMATION DISCLOSURE STATEMENT



Title of Invention

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN AN RF POWER AMPLIFIER

Application Number: 10/743221

\*10/743221\*

Confirmation Number: 7643

First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Art Unit: 2817

Search string: ( 6462620 or 6549071 ).pn

### US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

Init	Cite. No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6462620	2002-10-08	Dupuis et al.			
	2	6549071	2003-04-15	Paul et al.			

### Remarks

Note: Remarks are not for responding to an office action.

Applicants would also like to make the Examiner aware of the following commonly owned co-pending patent application: Serial No. 10/743,217, filed on 12/22/2003, attorney docket number SIL.P0064, entitled "CIRCUIT AND METHOD OF REDUCING NOISE IN AN RF POWER AMPLIFIER", by Alan L. Westwick et al. (pending).

### Signature

Examiner Name	Date

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Printed on: October 20, 2018

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U.S. PATENT & TRADEMARK OFFICE  
REFERENCE 1449  
PTO-1449

**PATENT APPLICATION**  
Page 1 of 1

ENRM PTO-1499

ATTY. DOCKET NO.	SIL-P0063	SERIAL NO.	10/743,221
APPLICANT	Westwick et al.		
FILING DATE	12/22/2003	GROUP	2817

**REFERENCE DESIGNATION**

U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLAS	TRANSLATION	
					YES	NO

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

T. Cho, E. Dukatz, M. Mack, D. Macnally, M. Marrisca, S. Mehta, C. Nilson, L. Plouvier, and S. Rubi, "A single-chip CMOS direct-conversion transceiver for 900MHz spread-spectrum digital cordless phones," IEEE International Solid-State Circuits Conference, vol. XLII, pp. 228 - 229, February 1999.

## **EXAMINER**

**DATE CONSIDERED**

Rev. 10/94 (Form 3.05)

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention:	CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN AN RF POWER AMPLIFIER				
Application Number:	10/743221 *10/743221*				
Confirmation Number:	7643				
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Art Unit:	2817				
Examiner:	Steven J. Mottola				
Search string:	( 3684975 or 3903485 or 4024346 or 4705967 or 5220209 or 5723994 or 5936485 or 5945878 or 6259325 or 6448847 or 6809581 or 20050134378 or 20050134386 or 20050134375 ).pn.				
<b>US Patent Documents</b>					
Note: Applicant is not required to submit a paper copy of cited US Patent Documents					
Init	Cite. No.	Patent No.	Date	Patentee	Kind
1	3684975	1972-08-01		Valfre, Cesare	
2	3903485	1975-09-01		Dolby, Ray Milton	
3	4024346	1977-05-01		McDermott, Noel C.	
4	4705967	1987-11-01		Vasile, Carmine F.	
5	5220209	2069-09-12		Seymour, Michael J.	
6	5723994	1998-03-01		Ting et al.	
7	5936485	1999-08-01		Rylov, Sergey V.	
8	5945878	1999-08-31		Westwick et al.	
9	6259325	2001-07-01		Ishizuka et al.	
10	6448847	2002-09-10		Paul et al.	
11	6809581	2004-10-01		Rofougaran et al.	

## US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

Init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
1		20050134378	2005-06-23	Westwick et al.			
2		20050134386	2005-06-23	Westwick et al.			
3		20050134375	2005-06-23	Westwick et al.			

## Remarks

Note: Remarks are not for responding to an office action.

A paper IDS is being submitted on the same date as this electronic IDS.

## Signature

Examiner Name	Date